

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>In re the Application of:</b> Holmes et al.	)	
	)	<b>Customer No.:</b> 29000
<b>Serial No.:</b> 10/708,717	)	
	)	<b>Confirmation No.:</b> 2716
<b>Filed:</b> 03/19/2004	)	
	)	<b>Group Art Unit:</b> 2838
<b>For:</b> OMNI VOLTAGE DIRECT	)	
CURRENT POWER SUPPLY	)	<b>Examiner:</b> R. B. Patel

**INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(b)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §§ 1.97 and 1.98, this Information Disclosure Statement is submitted for the purpose of bringing to the attention of the Office the items listed in the accompanying Form PTO-1449. Copies of the listed items, except for U.S. patent and U.S. patent application publications, are enclosed. The Examiner is requested to consider and make these items of information of official record in the Application.


The items identified in this Information Disclosure Statement (IDS) may or may not be "material" pursuant to 37 CFR §1.56 and the submission thereof by Applicants shall not be construed as an admission that any such patent, publication or other information referred to therein, or any combination thereof, is material or considered to be material (37 CFR §1.97(h)) or even qualifies as "prior art" under 35 USC §102 or §103 with respect to this invention unless specifically designated by Applicant as such.

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**CERTIFICATE OF TRANSMISSION**

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being transmitted via electronic filing (EFS-Web) on the date shown below to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 17, 2009  
Date of Transmission

  
Connie Kwon

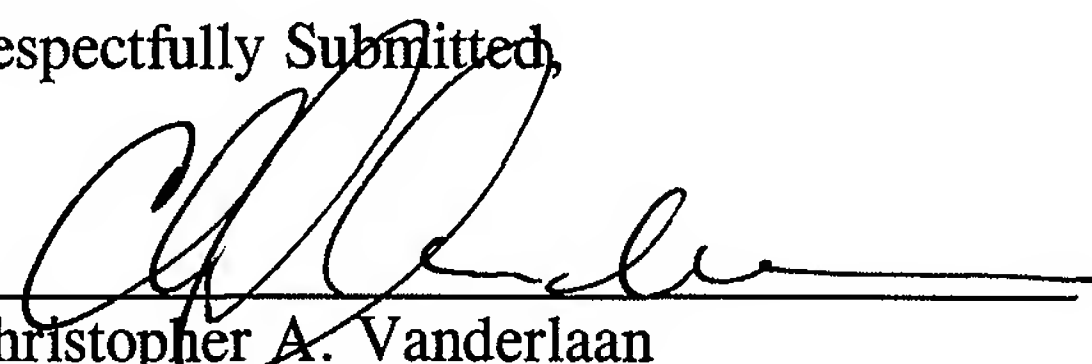
Furthermore, the submission of the above listed item(s) is not to be construed as a representation that an exhaustive search, or any search, has been made, or that additional information material to the examination of this Application does not exist.

The cited item (Reference AA) is a data sheet relating to LT1615 integrated circuit chip for a step-up DC/DC converter, which appears to be referenced in US Patent 6,791,283 (Bowman) cited in the Office Action dated August 14, 2008.

This IDS is believed to be timely in that it is being submitted under 37 CFR §1.97(b)(4), that is, before the mailing of a first Office action after the filing of a request for continued examination under § 1.114. Thus, no petition or fee is required. However, if the undersigned representative of Applicant is in error in this regard, then the Examiner is requested to consider this IDS as filed under §1.97(c) and is further authorized to charge any fee required by its filing to Deposit Account No. 09-0946.

Respectfully Submitted,

Dated: February 17, 2009

  
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